



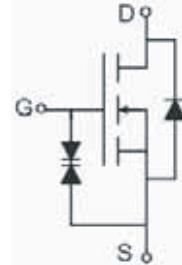
Leiditech

2N7002

N-Channel Enhancement Mode Power MOSFET

General Features

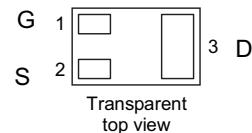
- $V_{DS} = 60V, I_D = 0.35A$
- $R_{DS(ON)} < 3.2\Omega @ V_{GS}=5V$
- $R_{DS(ON)} < 2.8\Omega @ V_{GS}=10V$
- ESD Rating: HBM 2300V
- Logic-level compatible
- High fast switching
- Halogen-free



Schematic diagram

Application

- Direct logic-level interface: TTL/CMOS
- Drivers: relays, solenoids, lamps, hammers, display, memories, transistors, etc.
- Switching circuits
- Solid-state relays



DFN1006-3

Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
72	2N7002	DFN1006	Ø180mm	8 mm	

Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($T_J = 150^\circ C$)	I_D	0.35	A
		0.2	
Drain Current-Pulsed ^(Note 1)	I_{DM}	0.8	A
Maximum Power Dissipation	P_D	0.35	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient ^(Note 2)	$R_{\theta JA}$	360(max)	W/°C
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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	60	68	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=60\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm 10\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	± 100	± 500	nA
		$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	± 4	± 10	μA
On Characteristics ^(Note 3)						
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	1	1.7	1.9	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=5\text{V}, \text{I}_D=0.2\text{A}$	-	2.5	3.2	Ω
		$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=0.2\text{A}$	-	2.2	2.8	Ω
Gate Resistance	R_g	$\text{V}_{\text{DS}}=0\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$	-	150	155	Ω
Forward Transconductance	g_{FS}	$\text{V}_{\text{DS}}=10\text{V}, \text{I}_D=0.2\text{A}$	0.1	0.25	0.28	S
Dynamic Characteristics ^(Note 4)						
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V}, f=1.0\text{MHz}$	-	21	50	PF
Output Capacitance	C_{oss}		-	5	10	PF
Reverse Transfer Capacitance	C_{rss}		-	4.2	5	PF
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	$t_{\text{d(on)}}$	$\text{V}_{\text{DD}}=30\text{V}, \text{I}_D=0.2\text{A}$ $\text{V}_{\text{GS}}=10\text{V}, \text{R}_{\text{GEN}}=10\Omega$	-	2.7	-	nS
Turn-on Rise Time	t_r		-	19	-	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	15	-	nS
Turn-Off Fall Time	t_f		-	23	-	nS
Total Gate Charge	Q_g	$\text{V}_{\text{DS}}=30\text{V}, \text{I}_D=0.2\text{A}, \text{V}_{\text{GS}}=10\text{V}$	-	3	5	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage ^(Note 3)	V_{SD}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_s=0.2\text{A}$	-	-	1.3	V
Diode Forward Current ^(Note 2)	I_s		-	-	0.2	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production

RATING AND CHARACTERISTICS CURVES

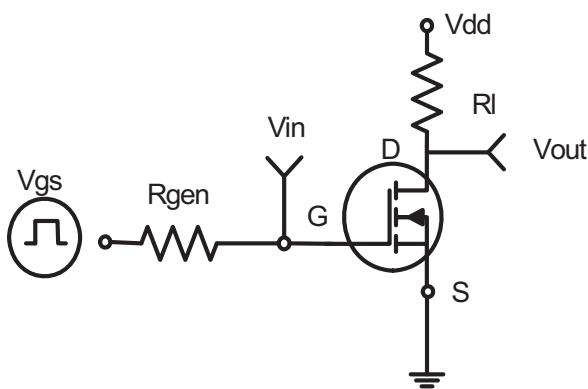


Figure 1: Switching Test Circuit

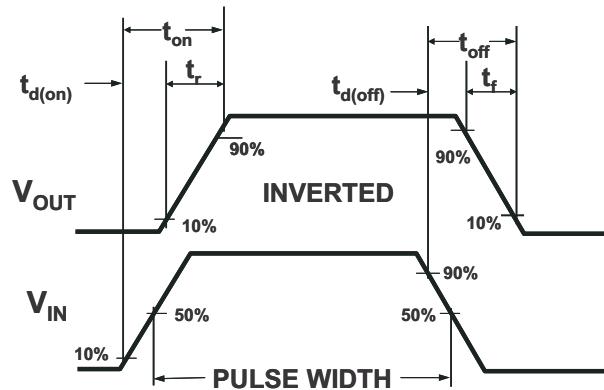


Figure 2: Switching Waveforms

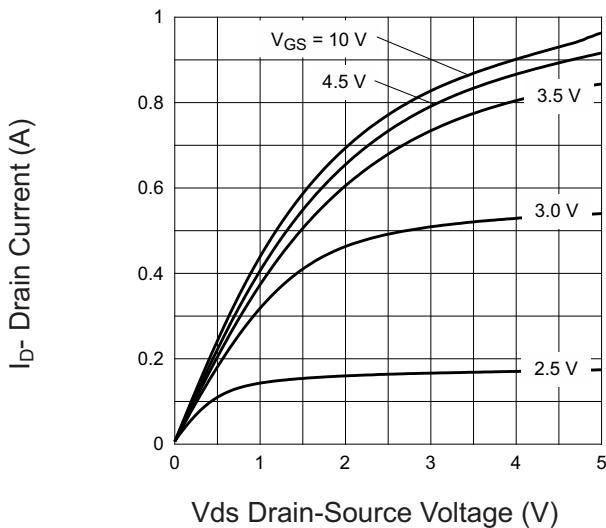


Figure 3 Output Characteristics

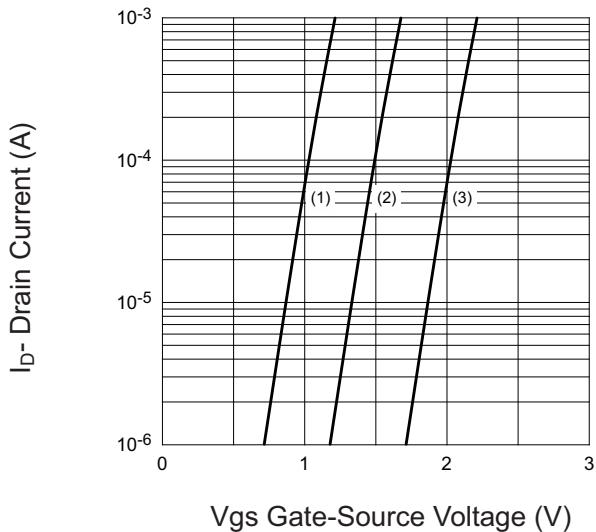


Figure 4 Transfer Characteristics

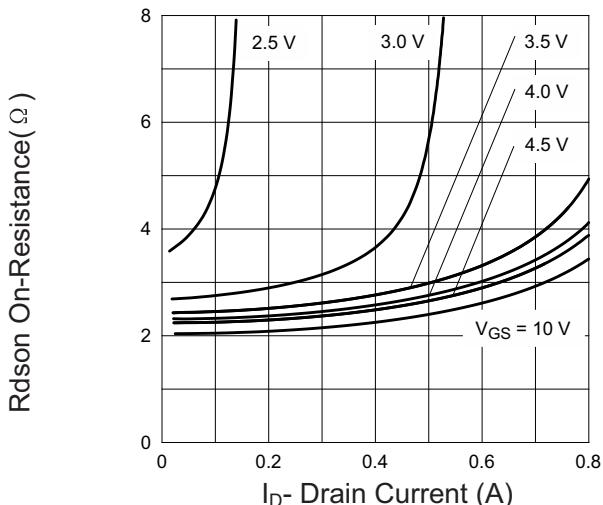


Figure 5 Drain-Source On-Resistance

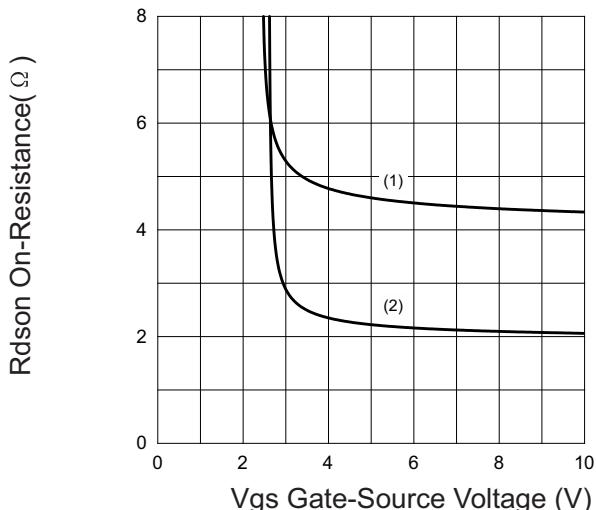


Figure 6 Rdson vs Vgs

RATING AND CHARACTERISTICS CURVES

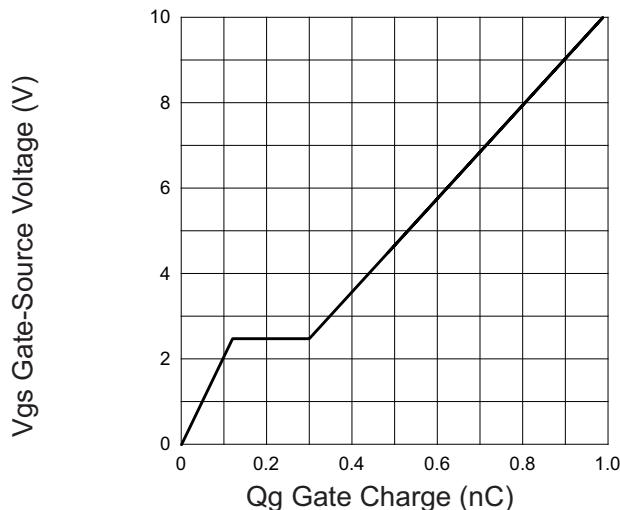


Figure 7 Gate Charge

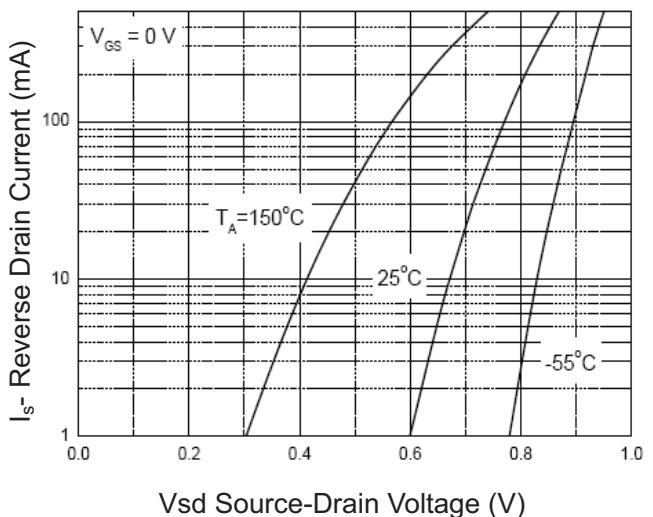


Figure 8 Source-Drain Diode Forward

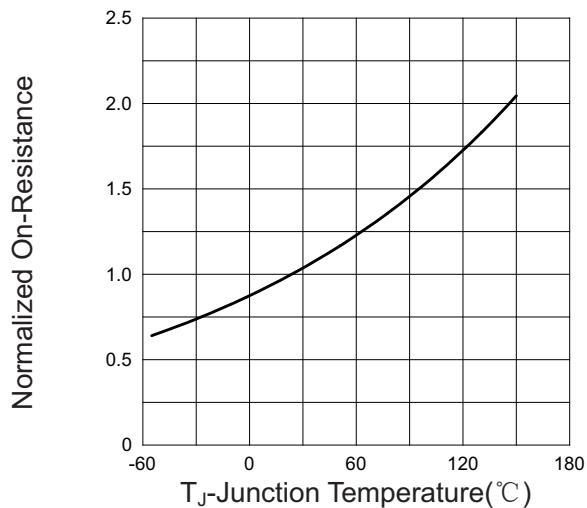


Figure 9 Drain-Source On-Resistance

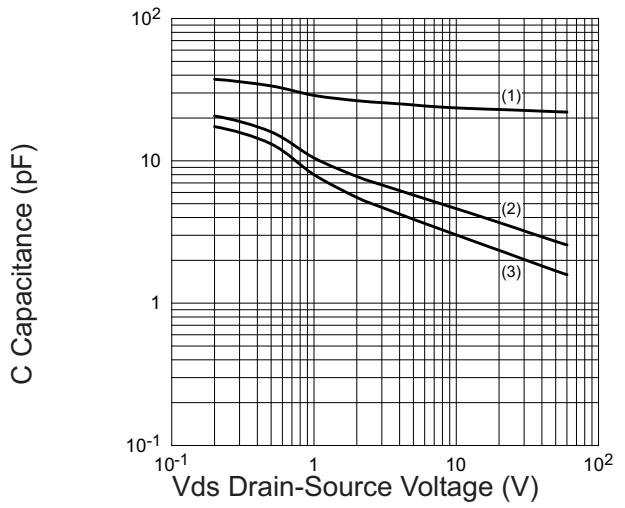


Figure 10 Capacitance vs Vds

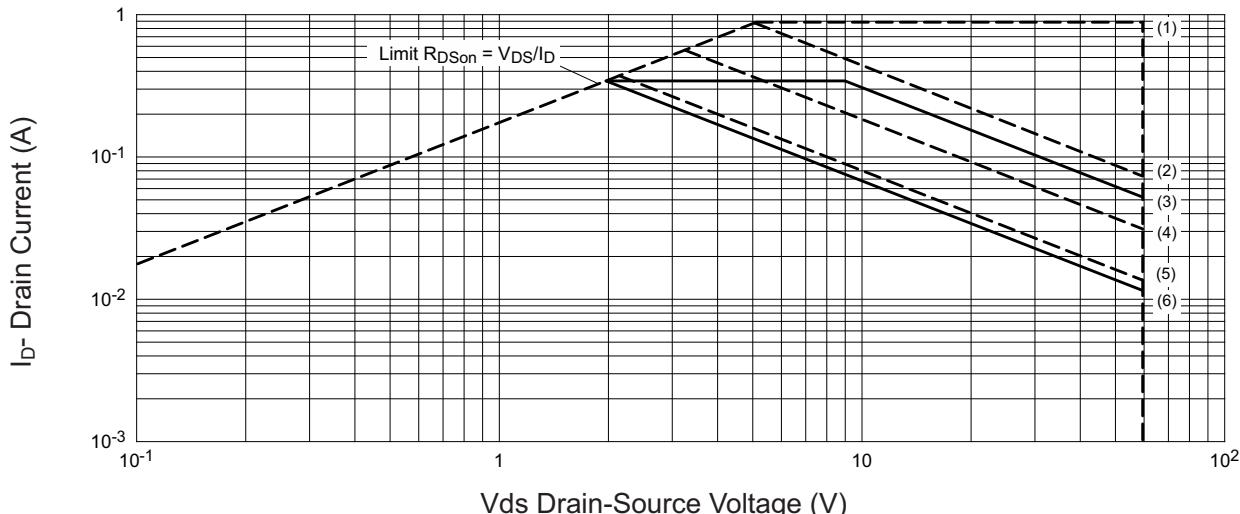
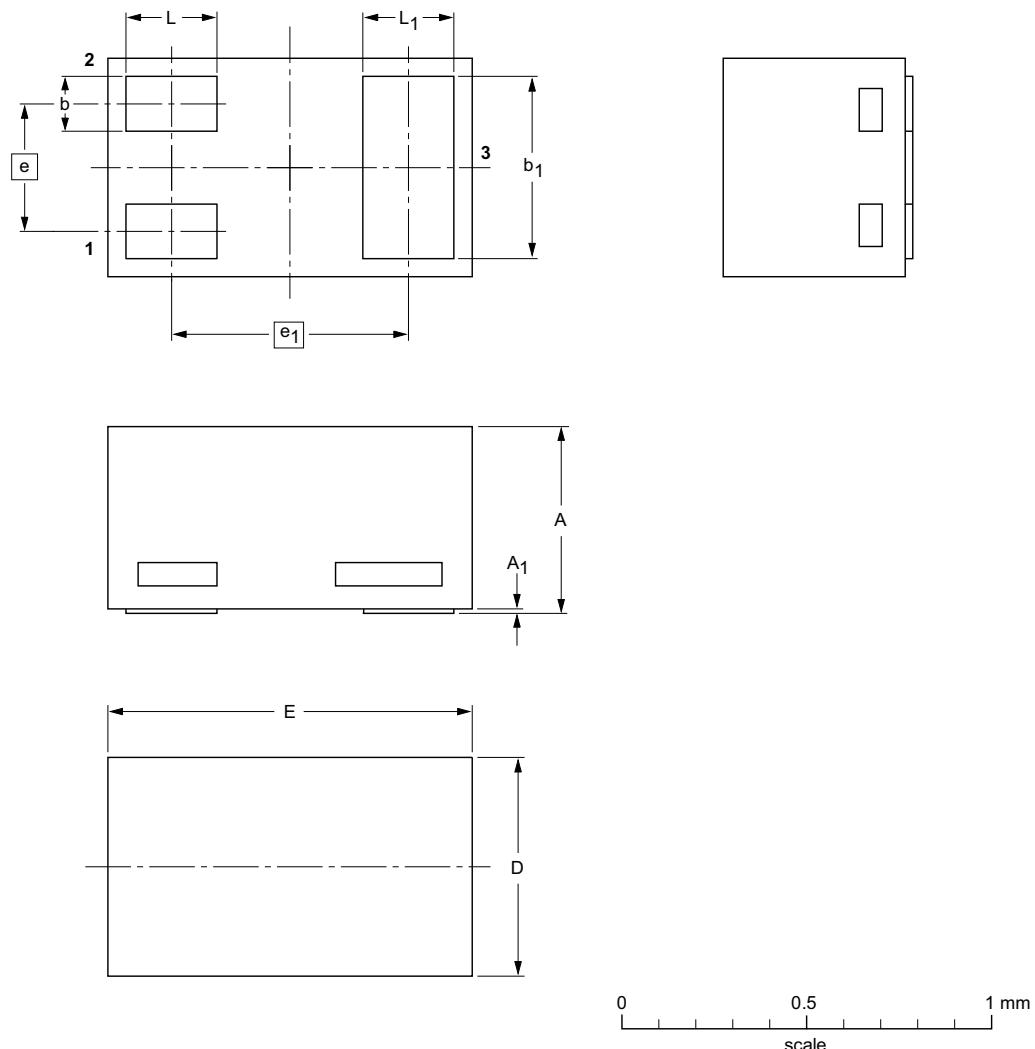


Figure 11 Safe Operation Area

DFN1006 Package Information

DIMENSIONS (mm are the original dimensions)

UNIT	A ⁽¹⁾	A ₁ max.	b	b ₁	D	E	e	e ₁	L	L ₁
mm	0.50 0.46	0.03	0.20 0.12	0.55 0.47	0.62 0.55	1.02 0.95	0.35	0.65	0.30 0.22	0.30 0.22

Package	Tube (pcs/tube)	Tube (pcs/inner box)	Tube (pcs/cartoon)	Tape&Reel (pcs/reel)	Tape&Reel (pcs/inner box)	Tape&Reel (pcs/cartoon)
DFN1006	—	—	—	10,000	10,000	

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